

DSD 2008: 11th Euromicro Conference On Digital System Design Architectures, Methods, And Tools Proceedings, September 3-5, 2008, Parma, Italy

by Euromicro Conference on Digital Systems Design Luca Fanucci EUROMICRO Universita di Parma

Untitled Our special focus will study a new class of massively parallel architectures we will call weakly-programmable . Compilation Techniques for CGRAs: Exploring All Parallelization Approaches. Proceedings of the 11th Euromicro Conference on Digital System Design (DSD), pp.345-352, Parma, Italy, September 3-5, 2008. 11th Euromicro Conference on Digital System Design: Architectures . The Euromicro Conference on Digital System Design (DSD) addresses all . engineering, down to microarchitectures, digital circuits and VLSI techniques. Publications - Distributed Embedded Systems - CCS Labs Proceedings of the 12th Euromicro Conference on Digital System Design (DSD) Architectures, Methods and Tools, IEEE Computer Society, Patras, Greece, pp. 729-734, Parma, Italy, September 3-5, 2008. Proceedings of the 11th IEEE Workshop on Design and Diagnostics of Electronic Systems (DDECS), IEEE DIGITAL SYSTEM DESIGN: ARCHITECTURES, METHODS AND . 10 Jul 2010 . Conference on Digital System Design Architectures, Methods and Tools, DSD 2008, September 3-5, 2008, Parma, Italy. of Self-Timed Logic In Synchronous Design, 11th EUROMICRO Conference on Digital Mesochronous Synchronizers in a NoC Switch Architecture, Proceedings of NoCs 2009. GALAXY Publications 1 Feb 2011 . Accueil Département SSE Publications 2008 observations ”, 11th Euromicro Conference on Digital System Design; Architectures, Methods and Tools (DSD08), ISBN: 978-0-7695-3277-6, pp. 709-713, Parma, Italy, September 3-5, 2008. CD-ROM Proceedings, Ricón, Puerto Rico, May 25-29, 2008. Architectures, Methods and Tools [6] D. Anderson et al., Evaluation of a Video-Based Fall Recognition System for Elders Using Applications, Digital System Design Architectures, Methods and Tools, 2008. DSD 08. 11th EUROMICRO Conference. Parma, Italy, 3-5 Sept. Proceeding on Telehealth and Assistive Technologies Conference – Apr. 2008 [12] B. CURRICULUM VITAE – English Version Dr - Microlab-NTUA Euromicro Conference on Digital System Design (DSD 2017) . Power-Aware Design Techniques of Secure Multi-Mode Embedded Systems 19th International Symposium, SAS 2012, Deauville, France, September 11-13, 11th EUROMICRO CONFERENCE on DIGITAL SYSTEM DESIGN (DSD 2008), Parma, Italy, Homepage of Sudip Roy - CSE IIT Kgp

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